TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (Ultra-High-Speed U-MOSIII)

TPCA8022-H

Switching Regulator Applications Motor Drive Applications DC/DC Converter Applications

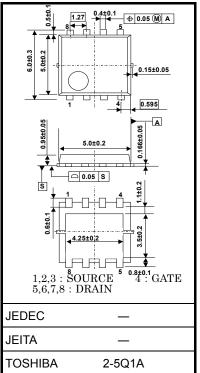
- Small footprint due to a small and thin package
- High speed switching
- Low drain-source ON-resistance

: RDS (ON) = 17 m Ω (typ.) (VGS=10V, ID=11A)

- High forward transfer admittance: $|Y_{fs}| = 46 \text{ S} (typ.)$
- Low leakage current: $I_{\rm DSS}$ = 10 μA (max) (V_{\rm DS} = 100 V)
- Enhancement mode: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

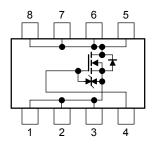
Absolute Maximum Ratings (Ta = 25°C)

Characte	eristic	Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	100	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR}	100	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	۱ _D	22	А	
Drain current	Pulsed (Note 1)	I _{DP}	66	~	
Drain power dissipati	on (Tc=25°C)	PD	45	W	
Drain power dissipation (t = 10 s) (Note 2a)		PD	2.8	W	
Drain power dissipation (t = 10 s) (Note 2b)		PD	1.6	W	
Single-pulse avalanche energy (Note 3)		E _{AS}	197	mJ	
Avalanche current		I _{AR}	22	A	
Repetitive avalanche energy (Note 2a) (Note 4)		E _{AR}	3.8	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature	range	T _{stg}	–55 to 150	°C	



Weight: 0.069 g (typ.)

Circuit Configuration



Note: For Notes 1 to 4, refer to the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.

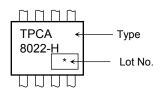
Unit: mm

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Thermal Characteristics

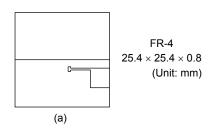
Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case (Tc=25°C)	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient $(t = 10 \text{ s})$ (Note 2a)	R _{th (ch-a)}	44.6	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	78.1	°C/W

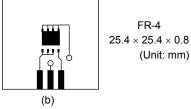
Marking (Note 5)



Note 1: The channel temperature should not exceed 150°C during use.

Note 2: (a) Device mounted on a glass-epoxy board (a)





Note 3: $V_{DD} = 50 \text{ V}$, $T_{ch} = 25^{\circ}C$ (initial), L = 0.5 mH, $R_G = 25 \Omega$, $I_{AR} = 22 \text{ A}$

Note 4: Repetitive rating: pulse width limited by max channel temperature

Note 5: * Weekly code: (Three digits)



Week of manufacture

(01 for first week of year, continuing up to 52 or 53)

Year of manufacture (The last digit of the calendar year)

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(b) Device mounted on a glass-epoxy board (b)

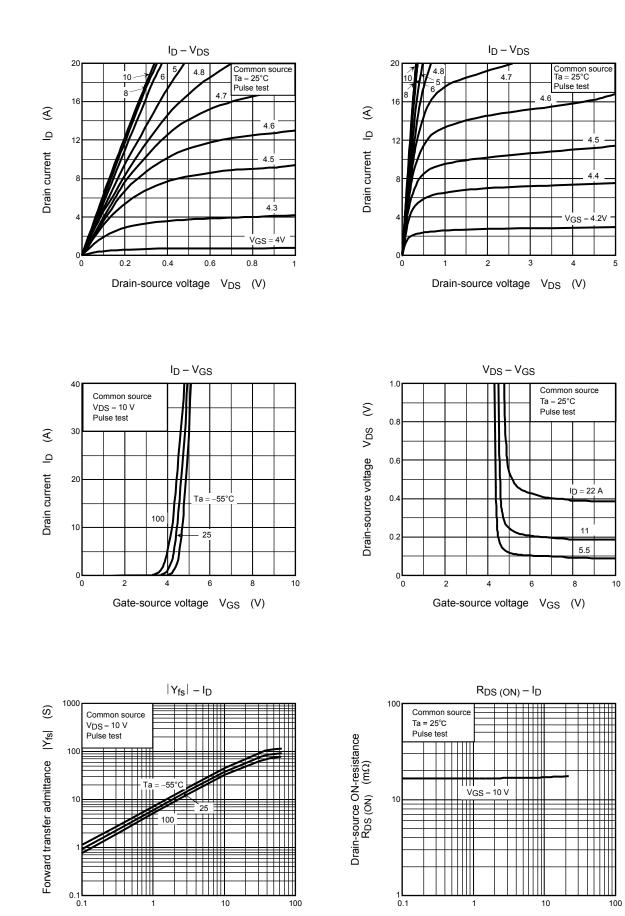
Electrical Characteristics (Ta = 25°C)

Cr	aracteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I _{GSS}	$V_{GS}=\pm 16~V,~V_{DS}=0~V$	_		±10	μA
Drain cutoff curre	ent	I _{DSS}	$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	_		10	μA
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	100		_	V
Gate threshold v	oltage	V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	2.0		4.0	V
Drain-source ON	-resistance	R _{DS (ON)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 11 \text{ A}$	_	17	26	mΩ
Forward transfer	admittance	Y _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 11 \text{ A}$	23	46	_	S
Input capacitance Reverse transfer capacitance		C _{iss}	- V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	2330	_	pF
		C _{rss}		_	110	_	
Output capacitar	Output capacitance			_	420	_	
Gate-Resistance		Rg		_	1.5	_	Ω
	Rise time	tr	$V_{GS} \stackrel{10}{\overset{0}{}_{V}} \bigvee \qquad I_{D} = 11 \text{ A}$	_	4.8	_	
	Turn-on time	t _{on}			14		20
Switching time	Fall time	tf			6.7	_	ns
	Turn-off time	t _{off}	$V_{DD}\simeq 50~V \label{eq:VDD}$ Duty \leq 1%, $t_W=$ 10 μs		42	_	
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \simeq 80 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 22 \text{ A}$		38		nC
Gate-source charge 1		Q _{gs1}		_	9.8	_	
Gate-drain ("Miller") charge		Q _{gd}		_	10	_	
Gate switch charge		Q _{SW}		_	14	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

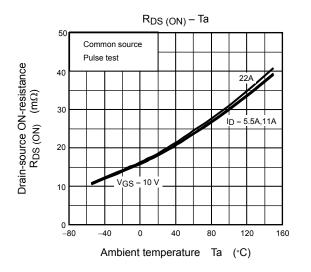
Character	istic	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse	I _{DRP}	—	_	_	66	А
Forward voltage (diode)		V _{DSF}	$I_{DR} = 22 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.2	V

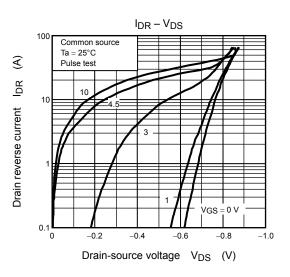
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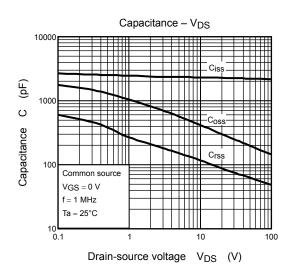


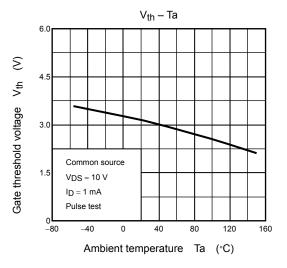
Drain current ID (A)

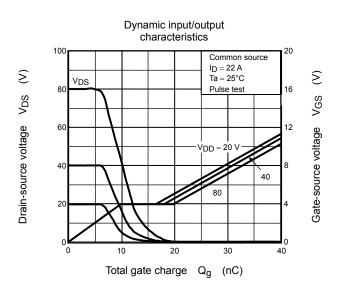
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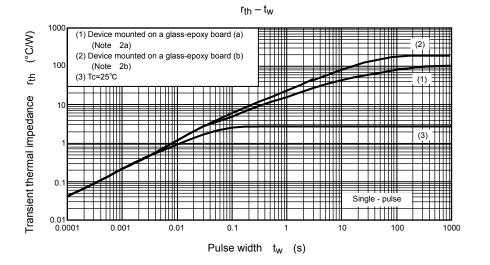


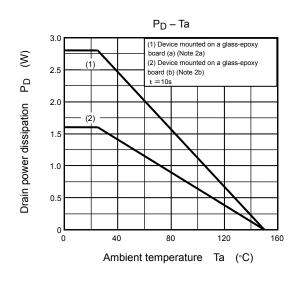


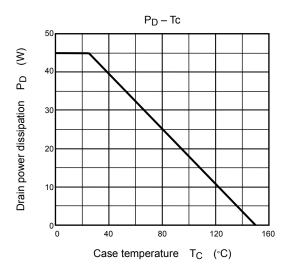


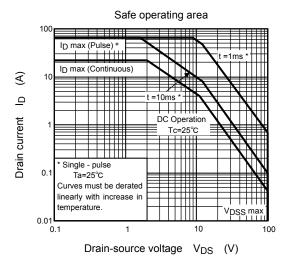












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